

27(14)/2013-IPHW
Government of India
Ministry of Communication & Information Technology
Department of Electronics & Information Technology (DeitY)

Date : 26-08-2013

ORDER

Subject: Constitution of Technical Evaluation Committee (MSIPS) for Light Emitting Diodes(LEDs) / Opto-Electronics under M-SIPS

Reference: 1. *MSIPS Policy Gazette Notification No. 175 dated 27-07-2012*
2. *MSIPS Guidelines dated 07-10-2012*

The Modified Special Incentive Package Scheme (M-SIPS) to offset disability and attract investment in Electronics System Design and Manufacturing(ESDM) in India was notified vide Notification no. 175 dated 27th July 2012 in Part-I, Section 1 of the Gazette of India (Extraordinary) [File no.- 24(10)/2012-IPHW]. As per para 6.2 of the notification, for effective functioning of M-SIPS a set of guidelines have been drawn and issued by the Department on dated 07-11-2012. [File no. 27(3)/2012-IPHW].

2. In furtherance of para 8 of the above cited guidelines, following Technical Evaluation Committee for products in the area of **Light Emitting Diodes(LEDs)/Opto-electronics** is constituted:

- i) Prof. K.B.R Verma, Material Research Center, IISc, Bangalore – **Chairman**
- ii) Shri Deepak Loomba, CMD, DeCore Group, Noida
- iii) Shri Rajeev Jindal, Head(R& D)-Lighting Business, Moser Baer India Ltd., Noida
- iv) Shri Varun Manwani, Sahasra Electronics Pvt Ltd, New Delhi
- v) Prof. Deepak Gupta, Department of Material Science & Engineering, IIT Kanpur
- vi) Prof. B.M. Arora, Department of Electrical Engineering, IIT Bombay
- vii) Dr. K. Chalpati, SAMEER, Mumbai
- viii) Shri Sandeep Chatterjee, Additional Director, DeitY, New Delhi- **Co-ordinator**

3. The Terms of Reference (TOR) of the Technical Evaluation Committee are as follows:

- i) **Name of committee:** Technical Evaluation Committee under M-SIPS (LED/Opto-Electronics)
- ii) **Scope:** The committee will provide recommendation regarding the technology proposed by an applicant and whether the said technology is 'State- of -the -art ' or not as per para 8 of M-SIPS Guidelines.

iii) **Deliverables:** The committee will provide its advise on the submissions with respect to the scope as defined above, made to the committee by the DeitY.

iv) **Mode of Operation :**

a. The normal mode of operation will be decision by way of circulation. The submissions may be circulated to all the committee members. The members may provide their views via email and the decision will be taken by consensus. However, whenever necessary a meeting may be convened with the approval of the Chairman.

b. The committee may seek additional technical information related to product or technology proposed.

v) **Co-opting :** DeitY may nominate other experts as necessary to the Technical Evaluation Committee.

vi) **TA/DA:** TA/DA for members for attending of meetings will be borne by the Department of Electronics & IT as per rules.

vii) **Conflict of Interest:** In the case of conflict of interest, it will be the responsibility of the members to inform the Chairman and withdraw himself/herself from the committee for that particular issue/case.

viii) **Role of DeitY representative**

The DeitY representative should act as co-ordinator of the committee. He shall be responsible for initiation of committee deliberations/meetings and facilitate logical conclusion of the various proposals in a time bound manner.

ix) **Timelines for TEC recommendations:**

For disposing the proposals in a time-bound manner, the timelines mentioned in MSIPS Guidelines cited above should be adhered by the committee. As per MSIPS Guidelines, the TEC should submit its recommendations within 15 days of the receipt of all the requisite technical write-up.



(Dr Ajay Kumar)
Joint Secretary
Tel : 24360160

To:

1. All Committee members

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1. All Ministries/Departments of Govt. of India
2. Cabinet Secretariat
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15/11/13
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